

Codasip selected to design a high-end RISC-V processor for the EU-funded DARE project

Thursday 6 March, 2025

Europe's RISC-V leader to provide a customizable general-purpose processor for the €240M initiative

Munich, Germany, 6 March 2025—Codasip, the European RISC-V leader, announced that it has been selected to provide a general purpose, high-end processor as part of the large-scale European supercomputing project Digital Autonomy with RISC-V in Europe (DARE).

DARE is set to build a supercomputing compute stack, featuring high-performance and energy-efficient RISC-V-based processors and accelerators designed and developed in Europe. The European Union has committed 240 million Euros in funding for the first 3-year program phase. The selected partners will leverage hardware/software co-design to achieve competitive performance and efficiency.

Funded by the project, Codasip will design a RISC-V-based General-Purpose Processor (GPP) that can be configured and customized for various HPC-class applications. The company is uniquely well-suited to deliver the GPP for the project, combining a fully Europe-based development team and an innovative Custom Compute approach.

Codasip's product portfolio currently spans a range from 32-bit low-power embedded to 64-bit application core families with optional safety and security features, including the advanced memory protection technology CHERI. The company offers extensive possibilities for hardware/software co-optimization and application-specific customization thanks to the highly automated processor design toolset Codasip Studio. Through the DARE project, Codasip will complete its product portfolio with an offering for high-end applications including AI, big data processing and supercomputing.

Karel Masarik, Codasip Founder and Chief Innovation Officer, said: "The development efficiency of our Custom Compute offering means we can do more with less compared to less powerful legacy approaches. We are excited to take part in the largest chip development project ever funded by the European Union. The EU sees the value of our technology in strengthening the European semiconductor industry. Through DARE and other projects, we also gain access to a European network of companies working together with us on very advanced new technologies."

Digital Autonomy with RISC-V in Europe (DARE) has received funding from the European High-Performance Computing Joint Undertaking (JU) under grant agreement No 101202459.

About Codasip

Codasip is a processor technology company enabling system-on-chip developers to differentiate their products for competitive advantage. Customers leverage the transformational potential of the open RISC-V ISA in a unique way through Codasip's Custom Compute offering: Codasip Studio design automation tools and a fully open architecture licensing model combine with a range of processor IP that can be easily customized. The company is proudly European and serves a global market, where billions of devices are already enabled by Codasip technology. Learn more at www.codasip.com

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