

vulnerabilities. It is backwards compatible and allows migration to safer code, and it makes C/C++ memory safe, avoiding costly software re-writes.

Codasip is standardizing a CHERI extension for RISC-V in collaboration with other members of [the CHERI Alliance](#), a community interest organization promoting the global adoption of CHERI security technology across the computing industry. Members of the alliance include the University of Cambridge, Google, the UK's National Cyber Security Centre (NCSC) and the UK's Defence Science and Technology Laboratory (Dstl).

Codasip X730 is the first commercial implementation of the new CHERI-RISC-V extension and is immediately available.

About Codasip

Codasip is a processor technology company enabling system-on-chip developers to differentiate their products for competitive advantage. Customers leverage the transformational potential of the open RISC-V ISA in a unique way through Codasip's Custom Compute offering: Codasip Studio design automation tools and a fully open architecture licensing model combine with a range of processor IP that can be easily customized. The company is proudly European and serves a global market, where billions of devices are already enabled by Codasip technology. Learn more at www.codasip.com

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Company Contact:

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Codasip

E. press@codasip.com

W. <https://codasip.com/>

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